

## Zoznam najvýznamnejších vedeckých prác, odborných prác alebo umeleckých prác, učebníc, učebných textov

### **Najvýznamnejšie publikované vedecké práce, verejne realizované alebo prezentované umelecké diela a výkony.**

JELEMENSKÁ, K. - SIEBERT, M. - MACKO, D. - ČIČÁK, P. Logic circuit design verification support tool - FitBoard. In WCETR 2011: Procedia - Social and Behavioral Sciences. Oxford : Elsevier, Vol. 28, No. 1-4, 2011, pp. 305-310. ISSN 1877-0428.

MOSTAFA, H. - ČIČÁK, P. Early Registration: Criterion to Improve Communication-Inter Agents in Mobil-IP Protocol. In International Journal of Information Technology, Vol. 3, No. 3 (2006), pp. 173-179. ISSN 1305-2403.

MOSTAFA, H. - ČIČÁK, P. Performance Simulation of a Mobile-IP Extension for Optimized Roaming Service. In Acta Electrotechnica et Informatica, Vol. 6, No. 1 (2006), pp. 11-19. ISSN 1335-8243.

BALGA, A. - SLÍŽIK, P. - ČIČÁK, P. - HLUCHÝ, L.: Hybrid Skipnet - Reducing Maintenance Overhead in a SkipNet-like Routing Structure. In Journal of Electrical Engineering, Vol. 55, No. 12 (2004), pp. 95-98. ISSN 1335-3632.

MOSTAFA, H. - ČIČÁK, P. Hands on Roaming Duration. petri-Nets Modeling of a Wireless Mobile-IP Procedure in Cisco Platform. In International Conference on Networking and Services (ICNS 2006), Silicon Valley, California, USA. - Los Alamitos : IEEE Computer Society, 2006. p. 6 CD-ROM. ISBN 0-7695-2622- 5.

### **Najvýznamnejšie publikované vedecké práce verejne realizované alebo prezentované umelecké diela alebo výkony za posledných šesť rokov.**

JELEMENSKÁ, K. - NOSÁL, M. - ČIČÁK, P. Visualization of Verilog Digital Systems Models. In Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering, Lecture Notes in Electrical Engineering. Springer Science+Business Media B.V., - New York : Springer, 2013, Vol. 151, pp. 805-818. ISSN 1876-1100.

JELEMENSKÁ, K. - ČIČÁK, P.- DÚCKY, V. Interactive presentation towards students' engagement. In Procedia - Social and Behavioral Sciences. ICEEPSY 2011 : International Conference on Education and Educational Psychology. Oxford : Elsevier, 2011, Vol. 29, No.1, pp. 1645-1653. ISSN 1877-0428.

JELEMENSKÁ, K. - ČIČÁK, P. - JURIKOVIČ, M.- PIŠTEK, P. Digital System Description Knowledge Assessment. In Technological Developments in Networking, Education and Automation. New York : Springer, 2010, pp. 127-132. ISBN 978-90-481-9150-5.

JAKAB, F. - BUČKO, M. - SIVÝ, I. - MADARÁSZ, L.- ČIČÁK, P. The System of Career Promotion of Networking Professional Based on Industrial Certificates. In INES 2009 - IEEE 13th International Conference on Intelligent Engineering Systems 2009, Barbados, April 16-18, 2009. - New York : IEEE, 2009. - ISBN 978-1-4244-4111-2 (<http://ieeexplore.ieee.org/xol/tocresult.jsp?isnumber=4924729>). pp. 221-226.

## **Výstupy v oblasti poznania príslušného študijného odboru s najvýznamnejšími ohlasmi a prehľad ohlasov na tieto výstupy.**

*Jurikovič, Matej - Čičák, Pavel - Jelemenská, Katarína: Parallel Controller Design and Synthesis. In: Proceedings of the 7th FPGAWorld Conference, September 6th, 2010, Copenhagen, Denmark : Academic Proceedings 2010. - New York : ACM, 2010. - ISBN 978-1-4503-0481-8. - S. 35-40*

Citované v: (3):

1. [o1] x2013 Alinguzhin, V. - Mari, F. - Melatti, I. - Salvo, I. - Tronci, E.: A map-reduce parallel approach to automatic synthesis of control software. In: *Lecture Notes in Computer Science*. - : Springer. - Vol. 7976 SPIN 2013. - , 2013, s. 43-60 (v **indexe Scopus**).
2. [o1] x2013 Pišteck, Peter - Marcinčín, Roman - Palaj, Tomáš - Štrba, Juraj: Logical Circuits Design Education Based on Virtual Verification Panel. In: *Lecture Notes in Electrical Engineering*. - ISSN 1876-1100. - Vol. 151 *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*. - New York : Springer Science-Business Media, 2013, s. 903-914 (v **indexe Scopus**).
3. [o3] x2013 Pišteck, Peter - Palaj, Tomáš - Marcinčín, Roman - Štrba, Juraj: Virtual verification panel as a tool in logic circuits design. In: *AWERProcedia. Information Technology and Computer Science. Vol. 1 : 2nd World Conference on Information Technology I (WCIT 2011), 23-27 November 2011, Antalya-Turkey. - Istanbul : Academic World Education & Research Center, 2013. - ISBN 2147-5105. - S. 296-301.*

*Jelemenská, Katarína - Jurikovič, Matej - Pišteck, Peter - Čičák, Pavel: Automated skills assessment in mobile environment. In: Research, Reflections and Innovations in Integrating ICT in Education, Vol.1, V. International Conference on Multimedia and Information & Communication Technologies in Education (m-ICTE 2009), Lisbon, Portugal : Vol. 1. - Badajoz : Formatex, 2009. - ISBN 978-84-692-1789-4. - S. 487-491*

Citované v: (4)

1. [o1] x2013 Genci, J.: A Few Reflections Regarding Assessment in an E-learning Environment. In: *Lecture Notes in Electrical Engineering*. - ISSN 1876-1100. - Vol. 151 *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*. - New York : Springer Science-Business Media, 2013, s. 437-445 (v **indexe Scopus**).
2. [o1] x2013 Genci, J.: Methods to Ensure Higher Variability of Knowledge Tests in the Moodle LMS Environment. In: *Lecture Notes in Electrical Engineering*. - ISSN 1876-1100. - Vol. 151 *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*. - New York : Springer Science-Business Media, 2013, s. 447-453 (v **indexe Scopus**).
3. [o3] x2012 Behúň, Michal: Automated HDL models evaluation. In: *AWER Procedia*. - ISSN 2147-5105. - *INSODE 2012 : 2nd World Conference on Innovation and Computer Science, 10-14 May 2012, Efes Sürmeli Hotel, Kuşadası-TURKEY. - Istanbul : Academic World Education & Research Center, 2012, s. 452-457.*
4. [o4] x2009 Giertl, Juraj - Révész, Martin - Kolcun, Peter - Jakab, František - Fecilak, Peter - Kopka, Jakub: Towards the Secure Using of Network-oriented Educational Systems. In: *ICETA 2009: 7th International Conference on Emerging eLearning Technologies and Applications, November 19-20, 2009, Stará Lesná, Slovakia. - Košice : Elfa, 2009. - ISBN 978-80-8086-128-5. - S. 71-76.*

*Jelemenská, Katarína - Tomalová, Elena - Čičák, Pavel: Knowledge assessment improvement in Moodle environment. In: Research, Reflections and Innovations in Integrating ICT in Education, Vol. 2, V. International Conference on Multimedia and Information & Communications Technologies in Education m-ICTE2009, Lisbon, Portugal. - Badajoz : Formatex, 2009. - ISBN 978-84-692-1790-0. - S. 979-983*

Citované v: (4)

1. [o1] x2013 Genci, J.: *A Few Reflections Regarding Assessment in an E-learning Environment. In: Lecture Notes in Electrical Engineering. - ISSN 1876-1100. - Vol. 151 Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering. - New York : Springer Science-Business Media, 2013, s. 437-445 (v indexe Scopus).*
2. [o1] x2013 Genci, J.: *Methods to Ensure Higher Variability of Knowledge Tests in the Moodle LMS Environment. In: Lecture Notes in Electrical Engineering. - ISSN 1876-1100. - Vol. 151 Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering. - New York : Springer Science-Business Media, 2013, s. 447-453 (v indexe Scopus).*
3. [o3] x2012 Behúň, Michal: *Automated HDL models evaluation. In: AWER Procedia. - ISSN 2147-5105. - INSODE 2012 : 2nd World Conference on Innovation and Computer Science, 10-14 May 2012, Efes Sürmeli Hotel, Kuşadasi-TURKEY. - Istanbul : Academic World Education & Research Center, 2012, s. 452-457.*
4. [o3] x2012 Masaryk, Tomáš - Behúň, Michal - Polák, Jakub: *Touch-screen input logic circuit simulator. In: AWER Procedia. - ISSN 2147-5105. - INSODE 2012 : 2nd World Conference on Innovation and Computer Science, 10-14 May 2012, Efes Sürmeli Hotel, Kuşadasi-TURKEY. - Istanbul : Academic World Education & Research Center, 2012, s. 458-463.*

*Jelemenská, Katarína - Siebert, Miroslav - Macko, Dominik - Čičák, Pavel: Logic circuit design verification support tool - FitBoard. In: Procedia - Social and Behavioral Sciences. - ISSN 1877-0428. - Vol. 28, No. 1-4, WCETR - 2011 : World Conference on Educational Technology Researches, Near East University, Cyprus Educational Sciences Association, 05-09 July, 2011 Nicosia / Kyrenia, North Cyprus. - Oxford : Elsevier, 2011, s. 305-310*

Citované v: (6)

1. [o1] x2012 Hlatký, Matej - Martinek, Valter - Gramatová, Elena: *D&T Presenter - Electronic Interactive System for Design and Test Education. - , 2012. In: DDECS 2012 : 15th IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems. Tallinn, Estonia, April 18-20, 2012. - Tallinn : University of Technology, 2012. - ISBN 978-1-4673-1188-5. - S. 175-178 (v indexe Scopus).*
2. [o3] x2012 Gramatová, Elena - Hlatký, Matej - Martinek, Valter: *A New Electronic Presentation System for Interactive Teacher's Work with Students. In: E-Learn 2012 : World Conference on E-learning in Corporate, Government, Healthcare, & Higher Education, October 9-12 Montréal, Québec. - Chesapeake : AACE, 2012. - ISBN 1-880094-98-3. - S. 598-603.*
3. [o3] x2012 Masaryk, Tomáš - Behúň, Michal - Polák, Jakub: *Touch-screen input logic circuit simulator. In: AWER Procedia. - ISSN 2147-5105. - INSODE 2012 : 2nd World Conference on Innovation and Computer Science, 10-14 May 2012, Efes Sürmeli Hotel, Kuşadasi-TURKEY. - Istanbul : Academic World Education & Research Center, 2012, s. 458-463.*
4. [o4] x2012 Hlatký, Matej - Martinek, Valter: *A New Electronic Presentation System for Design and Test. In: Student Research Conference 2012. Vol. 2 : 8th Student Research Conference in Informatics and Information Technologies Bratislava, April 25, 2012. - Bratislava : Nakladateľstvo STU, 2012. - ISBN 978-80-227-3690-9. - S. 305-310.*
5. [o4] x2012 Hlatký, Matej - Martinek, Valter: *A New Presentation System with Interactive Educational Activities. In: Information Sciences and Technologies. Bulletin of the ACM Slovakia. - ISSN 1338-1237. - Vol. 4, No. 2 (2012), s. 32-34.*

6. [o4] x2012 Polák, Jakub - Masaryk, Tomáš - Behúň, Michal: *Digital Circuit Design and Simulation System Using Gesture Recognition*. In: *Student Research Conference 2012*. Vol. 2 : 8th Student Research Conference in Informatics and Information Technologies Bratislava, April 25, 2012. - Bratislava : Nakladateľstvo STU, 2012. - ISBN 978-80-227-3690-9. - S. 323-328.

*Jelemenská, Katarína - Čičák, Pavel - Dúcky, Viktor: Interactive presentation towards students' engagement*. In: *Procedia - Social and Behavioral Sciences*. - ISSN 1877-0428. - Vol. 29, No.1 *ICEEPSY 2011 : The 2nd International Conference on Education and Educational Psychology*. - Oxford : Elsevier, 2011, s. 1645-1653

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1. [o1] x2012 Hlatký, Matej - Martinek, Valter - Gramatová, Elena: *D&T Presenter - Electronic Interactive System for Design and Test Education*. - , 2012. In: *DDECS 2012 : 15th IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems*. Tallinn, Estonia, April 18-20, 2012. - Tallinn : University of Technology, 2012. - ISBN 978-1-4673-1188-5. - S. 175-178 (v **indexe Scopus**).
2. [o3] x2012 Gramatová, Elena - Hlatký, Matej - Martinek, Valter: *A New Electronic Presentation System for Interactive Teacher's Work with Students*. In: *E-Learn 2012 : World Conference on E-learning in Corporate, Government, Helthcare, & Higher Education*, October 9-12 Montréal, Québec. - Chesapeake : AACE, 2012. - ISBN 1-880094-98-3. - S. 598-603.
3. [o3] x2012 Masaryk, Tomáš - Behúň, Michal - Polák, Jakub: *Touch-screen input logic circuit simulator*. In: *AWER Procedia*. - ISSN 2147-5105. - *INSODE 2012 : 2nd World Conference on Innovation and Computer Science*, 10-14 May 2012, Efes Sürmeli Hotel, Kuşadası-TURKEY. - Istanbul : Academic World Education & Research Center, 2012, s. 458-463.
4. [o3] x2013 Audi, Diana - Gouia-Zarrad, Rim: *A new dimension to teaching mathematics using iPads*. In: *IETC 2013 13th International Educational Technology Conferenc*, May 13-15, 2013 Kuala Lumpur, Malaysia : *Proceedings Book*. - Kuala Lumpur : IETC, 2013. - ISBN 2146-7366. - S. 70-72.
5. [o4] x2012 Hlatký, Matej - Martinek, Valter: *A New Electronic Presentation System for Design and Test*. In: *Student Research Conference 2012*. Vol. 2 : 8th Student Research Conference in Informatics and Information Technologies Bratislava, April 25, 2012. - Bratislava : Nakladateľstvo STU, 2012. - ISBN 978-80-227-3690-9. - S. 305-310.
6. [o4] x2012 Hlatký, Matej - Martinek, Valter: *A New Presentation System with Interactive Educational Activities*. In: *Information Sciences and Technologies. Bulletin of the ACM Slovakia*. - ISSN 1338-1237. - Vol. 4, No. 2 (2012), s. 32-34.

*Mostafa, Hossam el-ddin - Čičák, Pavel: Performance Simulation of a Mobile-IP Extension for Optimized Roaming Service*. In: *Acta Electrotechnica et Informatica*. - ISSN 1335-8243. - Vol. 6, No. 1 (2006), s. 11-19

Citované v: (2)

1. [o1] x2008 Giertl, Juraj - Baca, J. - Jakab, František - Andoga, Rudolf: *Adaptive sampling in measuring traffic parameters in a computer network using a fuzzy regulator and a neural network*. In: *Cybernetics and Systems Analysis*. - Vol. 44, No. 3 (2008), s. 348-356 (v **indexe Scopus**).
2. [o3] x2008 Giertl, Juraj - Bača, Ján - Jakab, František - Andoga, Rudolf: *Adaptivnyj otbor obrazcov pri izmerenii parametrov trafika kompjuternoj seti s ispol'zovanijem nečetskogo reguljatora i nejronnoj seti*. In: *Kibernetika i sistemnyj analiz*. - ISSN 0023-1274. - Vol. 43, No. 3 (2008), s. 46-54.

*Jelemenská, Katarína - Tomalová, Elena - Čičák, Pavel: Knowledge assessment improvement in Moodle environment. In: Research, Reflections and Innovations in Integrating ICT in Education, Vol. 2, V. International Conference on Multimedia and Information & Communications Technologies in Education m-ICTE2009, Lisbon, Portugal. - Badajoz : FormateX, 2009. - ISBN 978-84-692-1790-0. - S. 979-983*

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1. [o1] x2013 Genci, J.: A Few Reflections Regarding Assessment in an E-learning Environment. In: *Lecture Notes in Electrical Engineering*. - ISSN 1876-1100. - Vol. 151 *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*. - New York : Springer Science-Business Media, 2013, s. 437-445 (v **indexe Scopus**).
2. [o1] x2013 Genci, J.: Methods to Ensure Higher Variability of Knowledge Tests in the Moodle LMS Environment. In: *Lecture Notes in Electrical Engineering*. - ISSN 1876-1100. - Vol. 151 *Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering*. - New York : Springer Science-Business Media, 2013, s. 447-453 (v **indexe Scopus**).
3. [o3] x2012 Behúň, Michal: Automated HDL models evaluation. In: *AWERProcedia. Information Technology and Computer Science : 2nd World Conference on Innovation and Computer Science (INSODE-2012), 10-14 May 2012, Efes Sürmeli Hotel, Kuşadası-TURKEY. - Istanbul : Academic World Education & Research Center, 2012. - ISBN 2147-5105. - S. 452-457.*
4. [o3] x2012 Masaryk, Tomáš - Behúň, Michal - Polák, Jakub: Touch-screen input logic circuit simulator. In: *AWERProcedia. Information Technology and Computer Science : 2nd World Conference on Innovation and Computer Science (INSODE-2012), 10-14 May 2012, Efes Sürmeli Hotel, Kuşadası-TURKEY. - Istanbul : Academic World Education & Research Center, 2012. - ISBN 2147-5105. - S. 458-463.*

*Jelemenská, Katarína - Čičák, Pavel - Nosál, Michal: Visualization of Verilog Digital Systems Models. In: Lecture Notes in Electrical Engineering. - ISSN 1876-1100. - Vol. 151 Emerging Trends in Computing, Informatics, Systems Sciences, and Engineering. - New York : Springer Science-Business Media, 2013, s. 805-818*

Citované v: (1)

1. [o1] x2012 Katz, Shaun - Winberg, Simon - Mishra, Amit: Rapid Model-based Prototyping Tool for SDR on a RC Platform. In: *Proceedings of 2012 2nd IEEE International Conference on Parallel, Distributed and Grid Computing (PDGC), december 6-8, 2012, Himachal Pradesh, India. - Piscataway : IEEE, 2012. - ISBN 978-1-4673-2925-5. - S. 711-714 (v **indexe Scopus**).*

*Jelemenská, Katarína - Čičák, Pavel - Koine, Peter: The Pen-based Technology towards the Lecture Improvement. In: EDULEARN10, International Conference on Education and New Learning Technologies, Barcelona, 5-7 July, 2010 : Conference Proceedings CD. - Valencia : IATED, 2010. - ISBN 978-84-613-9386-2. - S. 6703-6712*

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1. [o1] x2012 Hlatký, Matej - Martinek, Valter - Gramatová, Elena: D&T Presenter - Electronic Interactive System for Design and Test Education. -, 2012. In: *DDECS 2012 : 15th IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems. Tallinn, Estonia, April 18-20, 2012. - Tallinn : University of Technology, 2012. - ISBN 978-1-4673-1188-5. - S. 175-178 (v **indexe Scopus**).*

2. [o3] x2012 Gramatová, Elena - Hlatký, Matej - Martinek, Valter: *A New Electronic Presentation System for Interactive Teacher's Work with Students. In: E-Learn 2012 : World Conference on E-learning in Corporate, Government, Helthcare, & Higher Education, October 9-12 Montréal, Québec. - Chesapeake : ACE, 2012. - ISBN 1-880094-98-3. - S. 598-603.*

*Csókás, Erik - Čičák, Pavel: Process visualization system design based on co-design methodology. In: ICETA 2007 : 5th Int. Conference on Emerging e-Learning Technologies and Applications. Stará Lesná, Slovak Republic, 6.-8.9.2007. - Košice : Elfa, 2007. - ISBN 978-80-8086-061-5. - S. 121-125*

Citované v: (1)

1. [o1] x2012 Macko, Dominik - Jelemenská, Katarína: *VHDL Visualizer: HDL Model Visualization with Simulation-Based Verification. In: DDECS 2012 : 15th IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems. Tallinn, Estonia, April 18-20, 2012. - Tallinn : University of Technology, 2012. - ISBN 978-1-4673-1188-5. - S. 199-200 (v indexe Scopus).*

*Čičák, Pavel - Kotočová, Margaréta - Grellneth, Igor - Mesjar, P: Cisco Certified Networking Professional (CCNP) - 4 steps to advanced networking Skills. In: ICETA 2005. 4th International Conference on Emerging e-learning Technologies and Applications. Košice, 14-15 September 2005. - Košice : Elfa, 2005. - ISBN 80-8086-016-6. - S. 453-455*

Citované v: (1)

1. [o1] x2010 Janitor, J. - Jakab, František - Kniewald, K.: *Visual Learning Tools for Teaching/Learning Computer Networks. In: 6th International Conference on Networking and Services, ICNS 2010, 7-13 March 2010 Cancun, Mexico. -, 2010. - ISBN 978-0-7695-3969-0. - S. 351-355 (v indexe Scopus).*

*Jelemenská, Katarína - Čičák, Pavel - Jurikovič, Matej - Pištek, Peter: Digital System Description Knowledge Assessment. In: Technological Developments in Networking, Education and Automation. - New York : Springer, 2010. - ISBN 978-90-481-9150-5. - S. 127-132*

Citované v: (1)

1. [o2] x2012 Genčí, Ján: *Possibilities to Solve Some of the Slovak Higher Education problems Using Information Technologies. In: ICETA 2012 [elektronický zdroj] : IEEE 10th International Conference on Emerging eLearning Technologies and Applications, November 8-9, 2012 Stará Lesná, The High Tatras, Slovakia. - Piscataway : IEEE, 2012. - ISBN 978-1-4673-5122-5 (<http://www.iceta.sk/proceedings/committees.htm>). - S. 117-120 (v indexe Web of Science, Scopus).*

### **Najvýznamnejšie vysokoškolské skriptá**

- BCI 1 - BURGER, I., NÁVRAT, P., ČIČÁK, P., KADLIC, A.: *Mikropočítačová technika. Bratislava, EFSVŠT 1984, 250 s. 12,5 AH*  
Autorský podiel je: **25%**, t.j. **3,125AH**
- BCI 2 - BURGER, I., NÁVRAT, P., ČIČÁK, P., KADLIC, A., DODEK, J., BÔRIK, J.: *Systémové programové vybavenie mikropočítačov, Bratislava DT ČSVTS, 1984, 156 s., 8,699 AH*  
Recenzent: Ing. Ivan Majer  
Náklad: 370  
Autorský podiel je: **28,85%**, t.j. **2,5AH**
- BCI 3 - FIŠTACKÝ, N., BURGER, I., ČIČÁK, P., KADLIC, A.: *Architektúra mikropočítačových systémov II. Bratislava, Inštitút pre výchovu vedúcich pracovníkov a EFSVŠT 1984, 170 s.*  
Autorský podiel je: **25%**
- BCI 4 - FRIŠTACKÝ, N., BURGER, I., ČIČÁK, P., KADLIC, A., NÁVRAT, P., HUDEC, L., HLADÍK, D., BLAŠKOVIČ, J.: *Mikropočítačové systémy s mikroprocesorom 8080. Bratislava, EFSVŠT 1984, 198 s., 15,14 AH*  
Recenzenti: doc. Ing. Karol Černík, CSc., Ing. Dušan Kodaj  
Náklad: 300 (v druhom vydaní 1988)  
Autorský podiel je: **30%**, t.j. **4,54AH**

### **Vysokoškolské učebnice vydané v domácich vydavateľstvách (ACB)**

- ACB 1 - BURGER, I., ČIČÁK, P., KADLIC, A.: *Cvičné programovanie na školskom mikropočítačovom systéme ŠMS VÚVT. Bratislava DT ČSVTS, 1982, 192 s., AH 9,681 AH*  
Recenzent: Ing. Ivan Majer  
Náklad: 2000  
Autorský podiel je: **40%**, t.j. **3,872AH**
- ACB 2 - ČIČÁK, P., KADLIC, A., VOJTEK, V.: *Základy výpočtovej techniky a programovania. Bratislava DT ČSVTS, 1988, 124 s., AH 6,304 AH*  
Recenzenti: Ing. Juraj Buša  
Náklad: 300  
Autorský podiel je: **50%**, t.j. **3,152AH**
- ACB 3 - BURGER, I., ČIČÁK, P., KADLIC, A.: *Mládež, elektronika, mikropočítače. SMENA, 1988. 164 s., AH 9,82 AH*  
Recenzenti: doc. Ing. Pavol Horváth, CSc., Ing. Pavol Návrat  
Náklad: 5000  
Autorský podiel je: **40%**, t.j. **3,928AH**

**Vedecká monografia vydaná v domácich vydavateľstvách (AAB)**

AAB 1 - **ČIČÁK, P.** – **ĎUDÁK, J.**: *Petriho siete a ich aplikácia v návrhu riadiacich a komunikačných systémov. ISBN 978-80-8086-233-6, Košice, ELFA, 2014. 199 s., 10,469 AH*

*Recenzenti: prof. Ing. Pavol Horváth, PhD., doc. Ing. František Jakab, PhD.*

*Náklad: 120*

*Autorský podiel je: 60%, t.j. 6,28 AH*